

Title: ADJUSTING DIE PLACEMENT ON A
SEMICONDUCTOR WAFER TO INCREASE
YIELD

Inventor: Eitan CADOURI
Application No.: Not Yet Assigned

Sheet 1 of 6

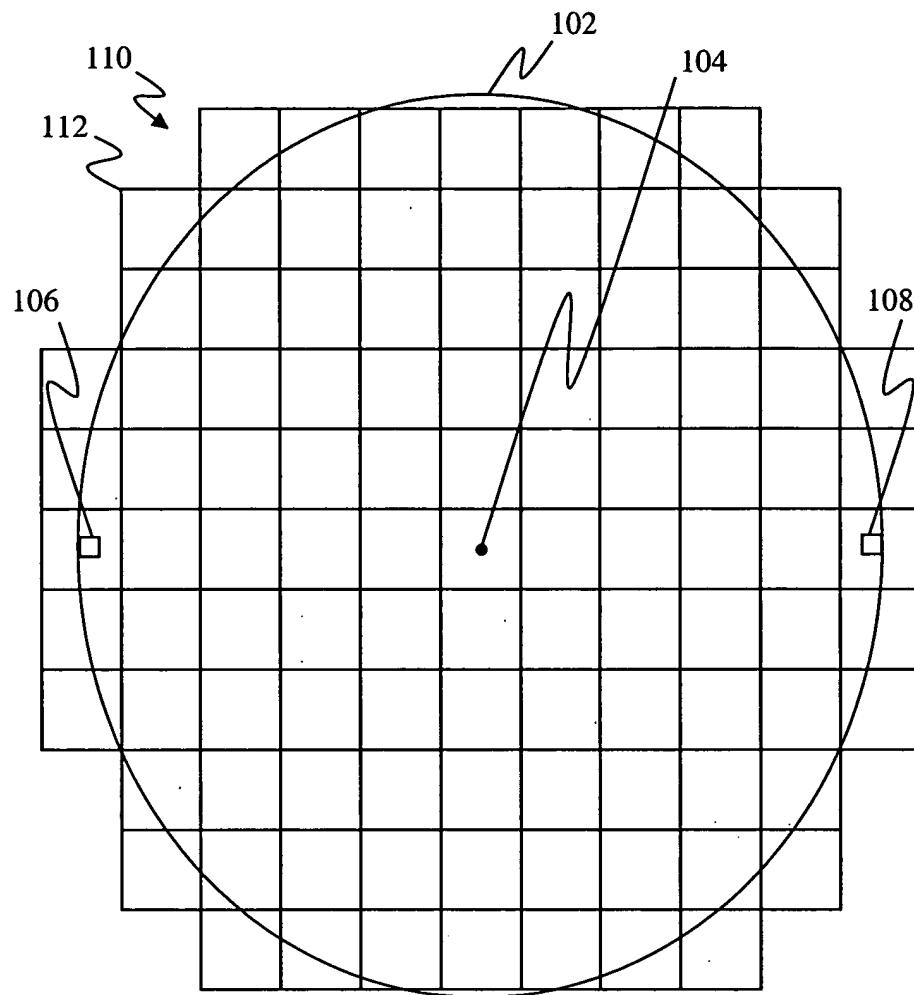


Fig. 1

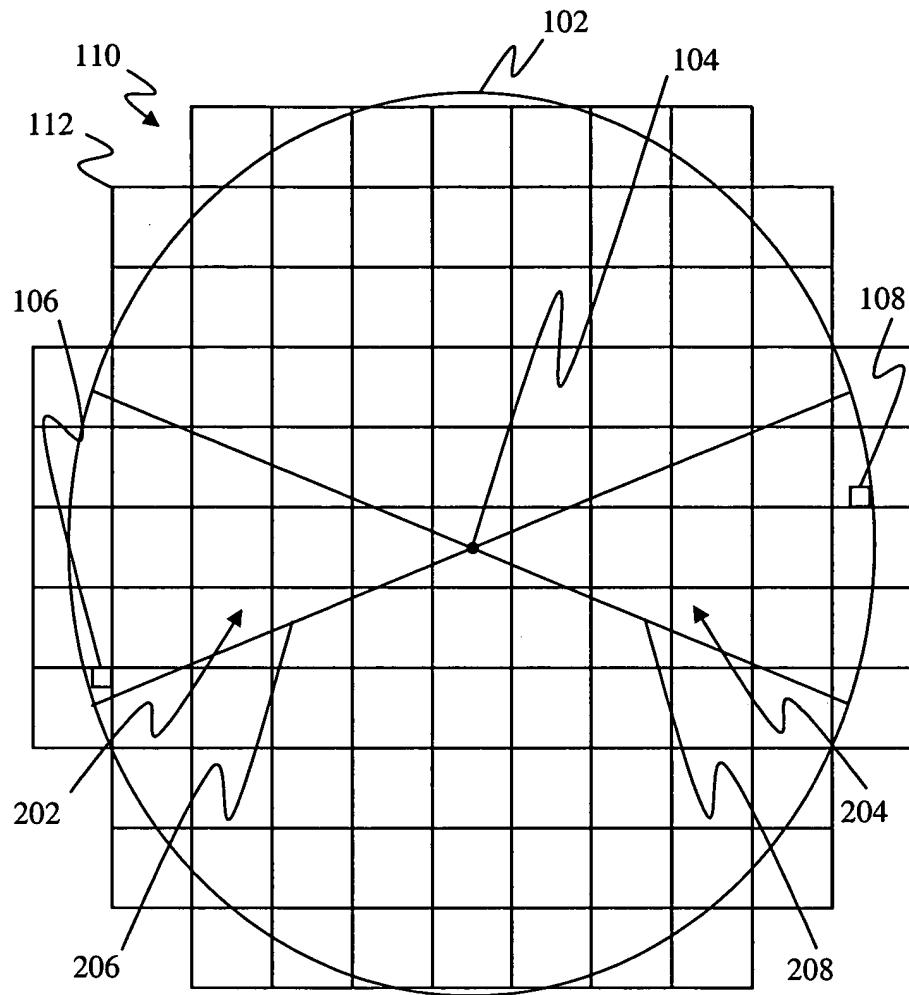


Fig. 2

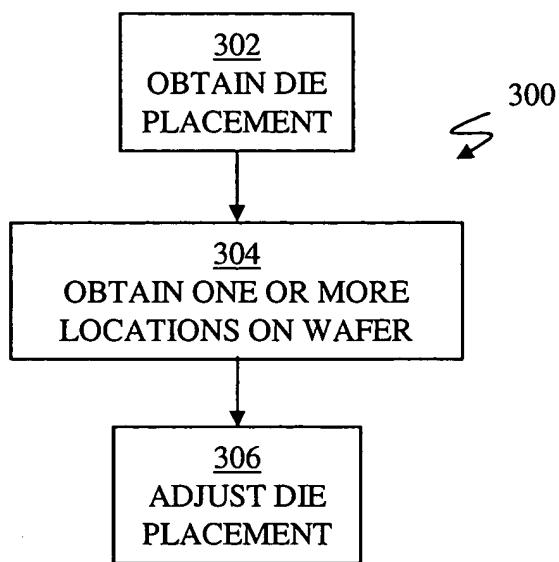


Fig. 3

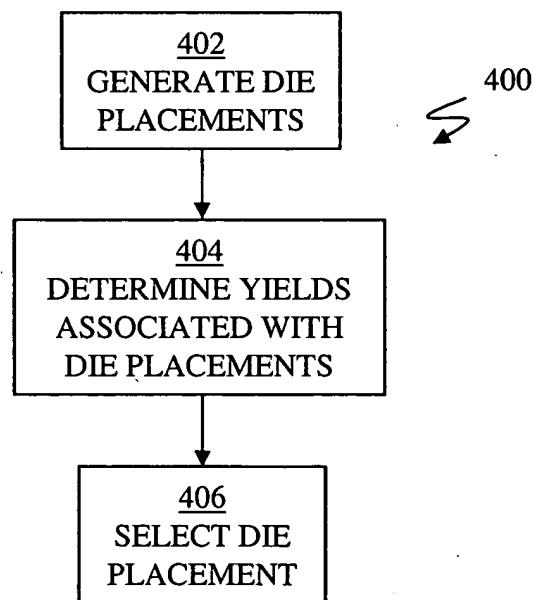


Fig. 4

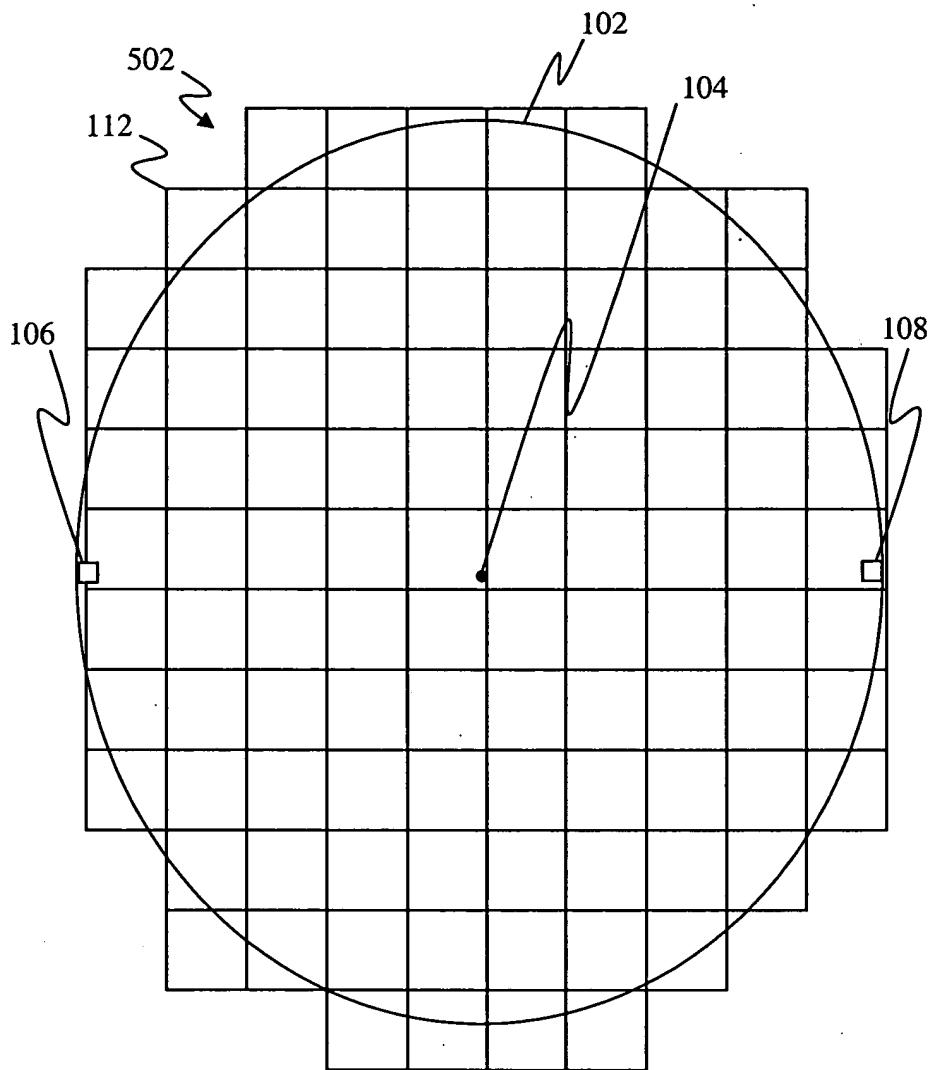


Fig. 5

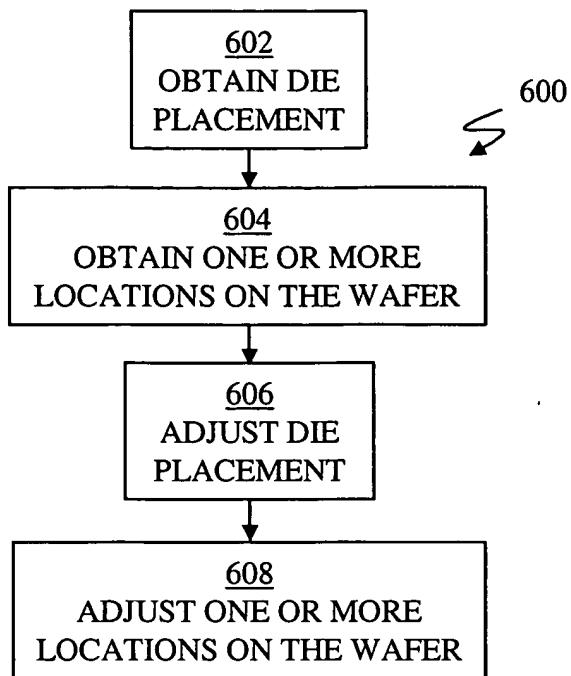


Fig. 6

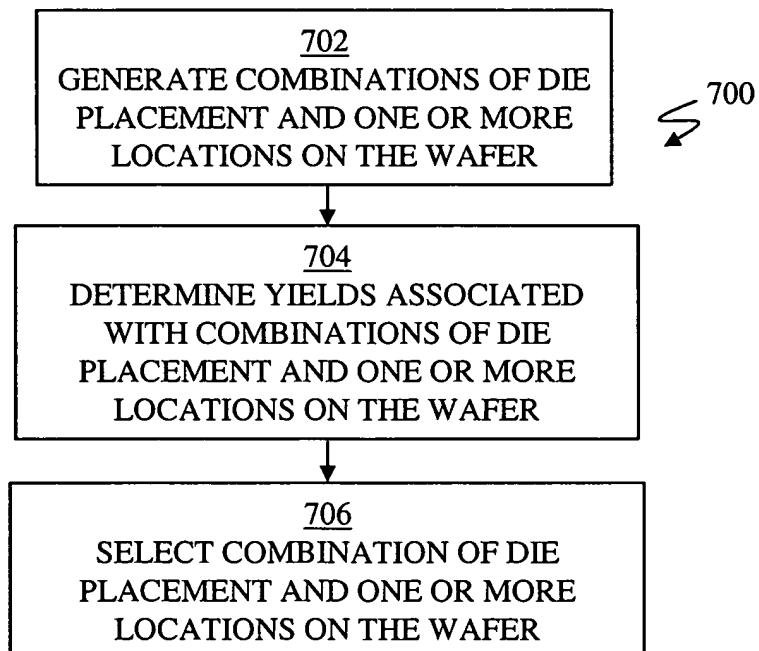


Fig. 7

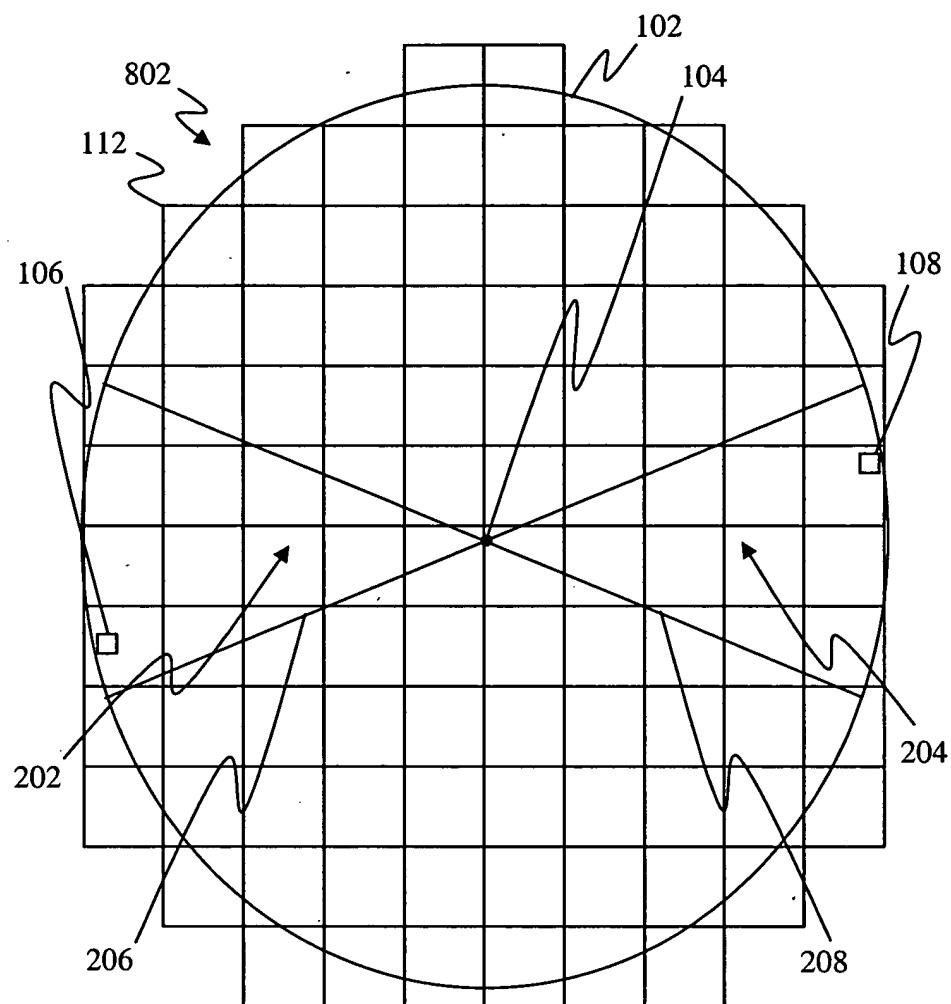


Fig. 8